



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**  
KAKINADA - 533 003 , ANDHRA PRADESH, INDIA

**CONSOLIDATED MARKS MEMO / CREDIT / GRADE SHEET**



CMM No. K **00632789**  
Serial No 419664  
Name: **GUDIMETLA RISHYENDRA**

Department of Technology in **ELECTRONICS & COMMUNICATION ENGINEERING**  
Name of the College - **SIR C.R. REDDY COLLEGE OF ENGINEERING**  
Name & Year of Final Exam **B.Tech May 2024**

Hall Ticket No **20B81A0464** Year of Admission **2020 - 2021** Class Awarded **First Class**

S.No	COURSE TITLE	GRADE	GRADE POINT	CREDITS	S.No	COURSE TITLE	GRADE	GRADE POINT	CREDITS
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**I YEAR**

1	MATHEMATICS - I	C	7	3.0	1	BASIC ELECTRICAL ENGINEERING	D	6	3.0
2	COMMUNICATIVE ENGLISH	C	7	3.0	2	MATHEMATICS-II	D	6	3.0
3	PROGRAMMING FOR PROBLEM SOLVING USING C	D	6	3.0	3	APPLIED PHYSICS	C	7	3.0
4	APPLIED CHEMISTRY	E	5	3.0	4	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	D	6	3.0
5	ENGINEERING DRAWING	E	5	3.0	5	NETWORK ANALYSIS	D	6	3.0
6	ENGLISH COMMUNICATION SKILLS LABORATORY	A	9	1.5	6	ENVIRONMENTAL SCIENCE	CP^	-	-
7	APPLIED CHEMISTRY LAB	A+	10	1.5	7	APPLIED PHYSICS LABORATORY	A	9	1.5
8	PROGRAM.FOR PROBLEM SOLVING USING C LAB	A	9	1.5	8	ELECTRONIC WORKSHOP LABORATORY	A	9	1.5
					9	BASIC ELECTRICAL ENGINEERING LABORATORY	A	9	1.5

**II YEAR**

1	MATHEMATICS-III	C	7	3.0	1	MANAGEMENT AND ORGANIZATIONAL BEHAVIOR	C	7	3.0
2	ELECTRONIC DEVICES AND CIRCUITS	D	6	3.0	2	ELECTRONIC CIRCUIT ANALYSIS	E	5	3.0
3	SWITCHING THEORY AND LOGIC DESIGN	D	6	3.0	3	DIGITAL IC DESIGN	D	6	3.0
4	SIGNALS AND SYSTEMS	D	6	3.0	4	ANALOG COMMUNICATIONS	D	6	3.0
5	RANDOM VARIABLES AND STOCHASTIC PROCESSES	D	6	3.0	5	LINEAR CONTROL SYSTEMS	D	6	3.0
6	OOPS THROUGH JAVA LAB	A+	10	1.5	6	CONSTITUTION OF INDIA	CP^	-	-
7	ELECTRONIC DEVICES AND CIRCUITS-LAB	A+	10	1.5	7	ELECTRONIC CIRCUIT ANALYSIS LAB	A	9	1.5
8	SWITCHING THEORY AND LOGIC DESIGN LAB	A	9	1.5	8	ANALOG COMMUNICATIONS LAB	A	9	1.5
9	PYTHON PROGRAMMING	A	9	2.0	9	DIGITAL IC DESIGN LAB	A	9	1.5
					10	SOFT SKILLS	A	9	2.0

**III YEAR**

1	ELECTROMAGNETIC WAVES & TRANSMISSION	E	5	3.0	1	DIGITAL SIGNAL PROCESSING	C	7	3.0
2	ANALOG ICS AND APPLICATIONS	D	6	3.0	2	PYTHON PROGRAMMING	C	7	3.0
3	DIGITAL COMMUNICATIONS	D	6	3.0	3	EMBEDDED SYSTEMS	B	8	3.0
4	COMPUTER ARCHITECTURE & ORGANIZATION	C	7	3.0	4	MICROPROCESSOR AND MICROCONTROLLERS	C	7	3.0
5	DATA BASE MANAGEMENT SYSTEMS	D	6	3.0	5	VLSI DESIGN	D	6	3.0
6	DATA STRUCTURES USING JAVA LAB	C	7	2.0	6	ARM BASED/AURDINO BASED PROGRAMMING	A+	10	2.0
7	SUMMER INTERNSHIP 2 MONTHS	A+	10	1.5	7	RESEARCH METHODOLOGY	CP^	-	-
8	COMMUNITY SERVICE PROJECT	A	9	4.0	8	DIGITAL SIGNAL PROCESSING LAB	A+	10	1.5
9	INDIAN TRADITIONAL KNOWLEDGE	CP^	-	-	9	VLSI DESIGN LAB	A+	10	1.5
10	ANALOG ICS AND APPLICATIONS LAB	A	9	1.5	10	MICROPROCESSOR AND MICROCONTROLLERS LAB	A+	10	1.5
11	DIGITAL COMMUNICATIONS LAB	C	7	1.5					

**IV YEAR**

1	CRYPTOGRAPHY & NETWORK SECURITY	D	6	3.0					
2	UNIVERSAL HUMAN VALUES 2:UNDERSTAND.HARMONY	C	7	3.0					
3	DIGITAL IMAGE PROCESSING	D	6	3.0					
4	DIGITAL IC DESIGN USING CMOS	C	7	3.0					
5	RADAR ENGINEERING	B	8	3.0					
6	INTRODUCTION TO INTERNET OF THINGS	B	8	3.0					
7	DESIGNER TOOLS (HFSS MICROWAVE STUDIO CST. CADENCE VIRTUOSO. SYNOPSYS MENTOR GRAPHICS XILINX.)	A+	10	2.0					
8	INDUSTRIAL/RESEARCH INTERNSHIP	A+	10	3.0	1	PROJECT WORK/SEMINAR & INTERNSHIP IN INDUSTRY	A+	10	8.0

Number of Credits registered for: **160**  
CGPA Secured: **7.34**

Date of Declaration of Result: **June 2024**  
(See overleaf for Instructions)

\* CP^ - Completed **19/9/2024**  
\* Medium of Instruction and Examinations in English

**CONTROLLER OF EXAMINATIONS**