JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET

B.Tech. COMPUTER SCIENCE & ENGINEERING

CMM. No. : c 0925867

Serial No. : 21345005401

Name : ASHUTOSH RANJAN

Hall Ticket No. : 15WJ1A05X1

Name of the College : WJ-G N I T C,KHANAPUR

Month & Year of Final Exam : May,2019

Year of Admission : 2015-2016

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
	I YEAR										
1	ENGLISH	18	35	53	4	2	MATHEMATICS - I	19	38	57	6
3	MATHEMATICAL METHODS	18	29	47	6	4	ENGINEERING PHYSICS	17	31	48	6
5	ENGINEERING CHEMISTRY	20	26	46	6	6	COMPUTER PROGRAMMING	20	26	46	6
7	ENGINEERING DRAWING	16	26	42	6	8	COMPUTER PROGRAMMING LAB	21	40	61	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	24	43	67	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	22	49	71	4
11	IT WORKSHOP / ENGINEERING WORKSHOP	22	42	64	4						
	I SEMESTER			**II YEAR**			**II SEMESTER**				
1	PROBABILITY AND STATISTICS	18	32	50	4	1	COMPUTER ORGANIZATION	18	26	44	4
2	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	16	26	42*	4	2	DATABASE MANAGEMENT SYSTEMS	19	32	51	4
3	DATA STRUCTURES	15	26	41*	4	3	JAVA PROGRAMMING	20	30	50	4
4	DIGITAL LOGIC DESIGN	21	26	47	4	4	ENVIRONMENTAL STUDIES	19	26	45	4
5	ELECTRONIC DEVICES AND CIRCUITS	17	46	63	4	5	FORMAL LANGUAGES AND AUTOMATA THEORY	21	35	56	4
6	BASIC ELECTRICAL ENGINEERING	20	26	46	4	6	DESIGN AND ANALYSIS OF ALGORITHMS	18	41	59	4
7	ELECTRICAL AND ELECTRONICS LAB	22	38	60	2	7	JAVA PROGRAMMING LAB	22	41	63	2
8	DATA STRUCTURES LAB	20	32	52	2	8	DATABASE MANAGEMENT SYSTEMS LAB	19	44	63	2
						9	GENDER SENSITIZATION* ^	20	42	62	2
	I SEMESTER			**III YEAR**			**II SEMESTER**				
1	PRINCIPLES OF PROGRAMMING LANGUAGES	21	30	51	4	1	DISTRIBUTED SYSTEMS	17	26	43	4
2	INTELLECTUAL PROPERTY RIGHTS	22	28	50	4	2	INFORMATION SECURITY	21	29	50	4
3	SOFTWARE ENGINEERING	19	37	56	4	3	OBJECT ORIENTED ANALYSIS AND DESIGN	22	33	55	4
4	COMPILER DESIGN	22	34	56	4	4	SOFTWARE TESTING METHODOLOGIES	22	29	51	4
5	OPERATING SYSTEMS	17	26	43	4	5	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	20	26	46	4
6	COMPUTER NETWORKS	20	30	50	4	6	WEB TECHNOLOGIES	21	30	51	4
7	OPERATING SYSTEMS LAB	22	43	65	2	7	CASE TOOLS AND WEB TECHNOLOGIES LAB	21	40	61	2
8	COMPILER DESIGN LAB	21	45	66	2	8	ADVANCED COMMUNICATION SKILLS LAB	22	46	68	2
	I SEMESTER			**IV YEAR**			**II SEMESTER**				
1	LINUX PROGRAMMING	19	26	45	4	1	MANAGEMENT SCIENCE	20	34	54	4
2	DESIGN PATTERNS	18	35	53	4	2	MULTIMEDIA & RICH INTERNET APPLICATIONS	20	36	56	4
3	DATA WAREHOUSING AND DATA MINING	18	26	44	4	3	AD HOC AND SENSOR NETWORKS	19	33	52	4
4	CLOUD COMPUTING	21	27	48	4	4	INDUSTRY ORIENTED MINI PROJECT	-	44	44	2
5	SOFTWARE PROJECT MANAGEMENT	19	28	47	4	5	SEMINAR	45	-	45	2
6	ARTIFICIAL INTELLIGENCE	22	26	48	4	6	PROJECT WORK	45	146	191	10
7	LINUX PROGRAMMING LAB	21	45	66	2	7	COMPREHENSIVE VIVA	-	88	88	2
8	DATA WAREHOUSING AND MINING LAB	22	42	64	2		(# Project Internal=50, External=150)				

Number of Credits registered for : 226 Aggregate Marks Secured for best : 218

Aggregate Marks Secured : 3158 OUT OF 5250 (60.15%)

Date of Issue : 10 July, 2019

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(^ Credits considered for award of degree)

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS